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Title: **JP8221906A2: DATA FORMING METHOD, DATA REPRODUCING METHOD, DATA FORMING DEVICE, DATA REPRODUCING DEVICE AND RECORDING MEDIUM**

Country: **JP Japan**  
 Kind: **A**

Inventor: **KANESHIGE TOSHIHIKO;  
KOJIMA TADASHI;**

Assignee: **TOSHIBA CORP  
TOSHIBA AVE CORP**  
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Published / Filed: **1996-08-30 / 1995-02-15**

Application Number: **JP1995000027005**

IPC Code: **G11B 20/12; G11B 20/10; H04L 7/08**

Priority Number: **1995-02-15 JP1995000027005**

Abstract:


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PURPOSE: To suppress the occurrence of a frame/sector shift by dividing a data block into plural columns of data frames and adding frame synchronizing signals intrinsic to each block or column to the head of the data frame and the remaining data frames, respectively.

CONSTITUTION: Digital information is divided into DBs so as to be arranged by a data blocking(DB) device 1 and, after adding ECC to the DB, converted to RLL code by means of a RLL encoder 3. The converted DB is made to be DF in a data framing(DF) device 4 and outputted to a sub-information adder 5. The adder 5 adds a frame synchronizing signal intrinsic to the block to the head data frame and a frame synchronizing signal intrinsic to the column to a remaining data frame. Consequently, the occurrence of a frame/sector shift is suppressed and the minimum code distance between the frame synchronizing signal and the information code is sufficiently secured.

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Family: None

Forward References:

PDF	Patent	Pub.Date	Inventor	Assignee	Title
<input checked="" type="checkbox"/>	<a href="#">US6314542</a>	2001-11-06	Nagai; Yutaka	Hitachi, Ltd.	<a href="#">Information recording method, reproducing method, and reproducing apparatus</a>
<input checked="" type="checkbox"/>	<a href="#">US6076184</a>	2000-06-13	Nagai; Yutaka	Hitachi, Ltd.	<a href="#">Information recording method, reproducing method, and reproducing apparatus</a>